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File: USPT

Jul 29, 1997

US-PAT-NO: 5652832

DOCUMENT-IDENTIFIER: US 5652832 A

TITLE: Method and apparatus for diagnosis and correction of peripheral device allocation faults

DATE-ISSUED: July 29, 1997

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP	CODE	COUNTRY
Kane; Edward S.	Framingham	MA	N/A		N/A
Bortcosh; Raouf W.	Natick	MA	N/A		N/A
Bajpai; Chandra K.	Stoughton	MA	N/A		N/A
Sansonetti, Jr.; Patrick J.	Wellesley	MA	N/A		N/A
Femino; Daniel P.	Brighton	MA	N/A		N/A

US-CL-CURRENT: 714/2; 714/26, 714/44

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Systems, Man and Cybernetics, Part B, IEEE Transactions on , Volume: 30 June 2000

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2 Burst error correction capabilities of turbo codes in mobile environments*Sanchez, M.P.; Shanmugan, K.S.; De Haro, L.; Calvo, M.*

3G Mobile Communication Technologies, 2000. First International Conference on , Volume: 1 Publ. No. 471) , 2000

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3 Monolithic GaAs-InGaP HBT balanced vector modulators for millimeter-wave wireless systems*Ashtiani, A.E.; Gokdemir, T.; Vilches, A.; Hu, Z.; Robertson, I.D.; Marsh, R.* Radio Frequency Integrated Circuits (RFIC) Symposium, 2000. Digest of Papers , 2000

Page(s): 187 -190

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4 A CSP model for Java multithreading*Welch, P.H.; Martin, J.M.R.*

Software Engineering for Parallel and Distributed Systems, 2000. Proceedings of the 10th International Symposium on , 2000

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